

Substitute for form 1449A/PTO

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**Application Number**

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**First Named Inventor:**

Wingyu Leung

**Examiner name:** unknown

**GROUP:** unknown

Sheet

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Attorney Docket Number

MST-012

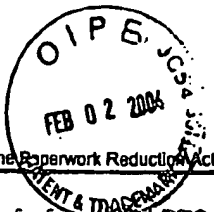
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Cite	Citation (Including Author, Title, Date, Pertinent Pages, Etc.)
C26J	"16-Bit CMOS Error Detection And Correction Unit", ©1990 Integrated Device Technology, Inc., pages 1-19
J	"Cost Analysis of On Chip Error Control Coding for Fault Tolerant Dynamic RAMs", by Najmi Jarwala and Dhiraj K. Pradham, 1987 IEEE, pages 278-283
J	"A Class of Optimal Minimum Odd-weight-column SEC-DED Codes" by M.Y. Hsiao, July 1970, pages 395-401

**EXAMINER**

DATE CONSIDERED

<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.



PTO/SB/08A (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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<b>Substitute for form 1449 A/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		<b>Application Number</b>	10/003,602
		<b>Filing Date</b>	11/14/2001
		<b>First Named Inventor:</b>	Wingyu Leung
		<b>Examiner name: A. Decady</b>	GROUP: 2133
<b>Sheet</b>	1 of 1	<b>Attorney Docket Number</b>	MST-012

**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cited No. <sup>1</sup>	Document Number Number - Kind Code <sup>2</sup> (/ known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear
CJS	B1	US-5,524,220	06-04-1996	Verma et al.	
↓	B2	US-6,147,535	11-14-2000	Leung	
		US-			
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		US-			
		US-			

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**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Cntry Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (/ known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear	T <sup>6</sup>
CJS	B3	GB 2289779	11-29-1995	Intel Corporation		

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

CJS	"Error detection and correction with the IDT49C466" by Anupama Hegde, 12/18/1994, No. 10, pages 613-620.
CJS	"Error Correction Circuitry for Dynamic Random Access Memories", IBM Technical Disclosure Bulletin, Vol. 36, No. 08, August 1993, pages 681-682.

EXAMINER SIGNATURE: *Christina*

DATE CONSIDERED: 8/6/04

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